

PA-G3P-FCD(P) Data Sheet

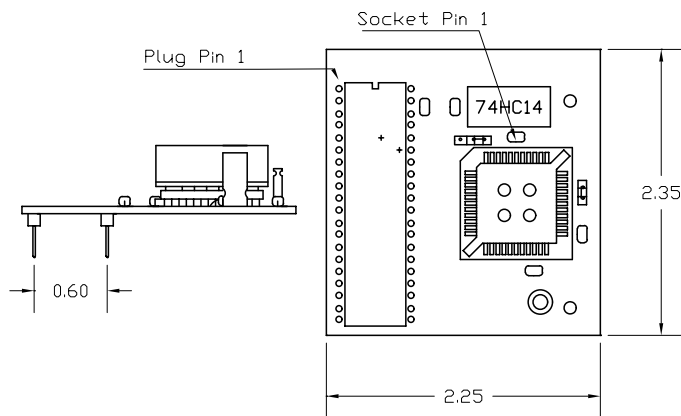
44 pin PLCC socket/40 pin DIP 0.6" plug

Supported Device/Footprints

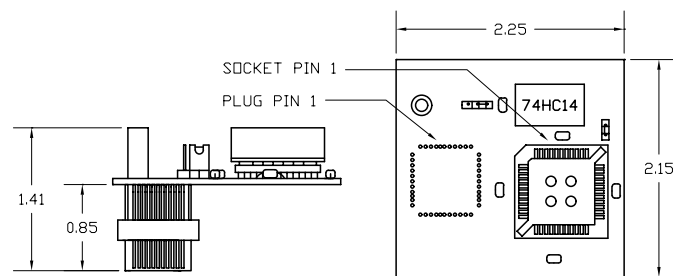
These adapters accept 44 pin PLCC versions of the Philips 51 XAG3 and plug into an 80C51 using either a 40 pin DIP, or a 44 pin PLCC footprint.

Manufacturer	Device		Footprint	
	Device	Device	Device	Package
Philips	51XA-G3PLCC	80C51	40 DIP 0.6"	or 44 PLCC

Adapter Dimensions



PA-G3P-FCD



PA-G3P-FCP

Memory Map????

Both the 51XA-G3 and the 80C51FC (FC) contain 32K bytes of EPROM. Using the FC algorithm all of the 51XA-G3's EPROM (0000-7FFF) can be programmed.

Adapter Parts & Part Numbers

The following chart shows the various socket and board part numbers that make up these adapters.

Adapter	Production Socket	Circuit Board
PA-G3P-FCD	44-106 or 44-306	XAG3-FC
PA-G3P-FCP	44-106 or 44-306	XAG3-FC

Adapter Construction

The adapter is made up of 3 sub-assemblies. They assemble via connectors making the adapter modular. This way the sub-assemblies can be replaced when they wear out.

When disassembling the adapter take care not to bend the pins. When reassembling the adapter note the pin 1 indicators to align the parts correctly.

PLCC Socket

PLCC Production socket:

PA-G3P-FCD, -P

Accepts the test socket and performs the wiring shown in the Adapter Wiring section.



Adapters-Plus Inc.

P.O. BOX 900
Tracy, Ca 95378
ph.209-839-0200
fax.209-839-0235
email: sales@adapt-plus.com

www.adapt-plus.com

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Rev 2/18/97
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Adapter Wiring

The following chart shows the connections from the PLCC device to the adapter's DIP plug.

Adapter Socket		Adapter Plug	
51XAG3-PLCC		80C51-DIP	
Pin	Signal	Pin	Signal
1	VSS	20	VSS
2	P1.0	1	P1.0
3	P1.1	2	P1.1
4	P1.2	3	P1.2
5	P1.3	4	P1.3
6	P1.4	5	P1.4
7	P1.5	6	P1.5
8	P1.6	7	P1.6
9	P1.7	8	P1.7
10	RST*	9**	RST
11	P3.0	10	RXD
12	-	-	N/C
13	P3.1	11	TXD
14	P3.2	12	INT0*
15	P3.3	13	INT1*
16	P3.4	14	T0*
17	P3.5	15	T1*
18	P3.6	16	WR *
19	P3.7	17	RD*
20	XTAL2	18	X2
21	XTAL1	19	X1
22	VSS	20	VSS
23	VDD	40	VCC
24	P2.0	21	P2.0
25	P2.1	22	P2.1
26	P2.2	23	P2.2
27	P2.3	24	P2.3
28	P2.4	25	P2.4
29	P2.5	26	P2.5
30	P2.6	27	P2.6
31	P2.7	28	P2.7
32	PSEN*	29	PSEN*
33	ALE/PGM*	30	ALE/PGM*
34	-	-	N/C
35	EA*/VPP	40*	VCC
36	P0.7	32	P0.7
37	P0.6	33	P0.6
38	P0.5	34	P0.5
39	P0.4	35	P0.4
40	P0.3	36	P0.3
41	P0.2	37	P0.2
42	P0.1	38	P0.1
43	P0.0	39	P0.0
44	VDD	40	VDD

*Must be connected via JP2

**Optional inverter via JP1

DIP Pin 31(EA*/VPP) is unconnected



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