

PA711K4-QE Data Sheet

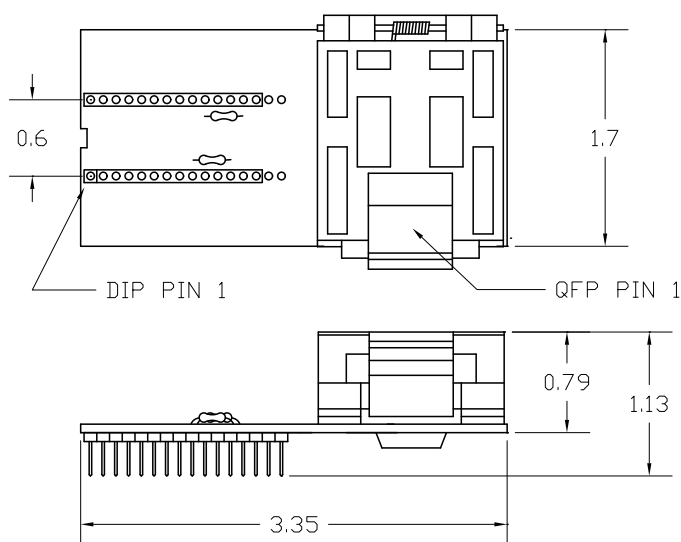
80 pin QFP socket/28 pin DIP 0.6" plug

Supported Device/Footprints

This adapter supports programming OTP or EPROM in Motorola 68HC11K4 devices on 28 pin DIP programmers. The devices must be packaged in the 80 pin QFP package (FU80).

Device			Programmer	
Mfgr	Device	Package	Device	Plug
Motorola	68HC711K4	QFP	27C256	28 pin DIP

Adapter Dimensions



Adapter Construction

The adapter is made up of 2 sub-assemblies. They assemble via connectors making the adapter modular. This way the sub-assemblies can be replaced when they wear out. When disassembling the adapter take care not to bend the pins. When reassembling the adapter note the pin 1 indicators to align the parts correctly.

Test Socket

QFP test socket:

Yamaichi Part #: IC120-0804-711 LSC Part #: 80-711

K4-80-28

Accepts the test socket and performs the wiring shown in the Adapter Wiring section.

Memory Access

Address	Size	711K4	27C256 programmer
A000-FFFF	24K Bytes	EPROM	Pgm/Read/Write 2000-7FFF

Adapter Wiring

The following chart shows the connections from the QFP device to the adapter's DIP plug. *14 = 10K Pulldown

DEVICE	SIGNAL	PLUG	PLUG	SIGNAL	DEVICE
1	GND	14	14	GND	80
2	GND	14	*14	PD1	79
3	GND	14	*14	PD0	78
4	GND	14	14	GND	77
5	GND	14	14	GND	76
6	GND	14	*14	RST-	75
7	GND	14	-	N/C	74
8	GND	14	*14	EXTAL	73
9	GND	14	-	N/C	72
10	GND	14	28	VCC	71
11	GND	14	14	GND	70
12	VCC	28	19	D7	69
13	GND	14	18	D6	68
14	OE-	22	17	D5	67
15	A14	27	16	D4	66
16	A13	26	15	D3	65
17	A12	2	13	D2	64
18	A11	23	12	D1	63
19	A10	21	11	D0	62
20	A9	24	20	CE-	61
21	A8	25	10	A0	60
22	GND	14	9	A1	59
23	GND	14	8	A2	58
24	GND	14	7	A3	57
25	GND	14	6	A4	56
26	GND	14	5	A5	55
27	GND	14	4	A6	54
28	GND	14	3	A7	53
29	GND	14	14	GND	52
30	VPP	1	14	GND	51
31	VCC	28	14	GND	50
32	GND	14	-	N/C	49
33	GND	14	-	N/C	48
34	GND	14	-	N/C	47
35	GND	14	-	N/C	46
36	GND	14	-	N/C	45
37	GND	14	-	N/C	44
38	GND	14	-	N/C	43
39	GND	14	-	N/C	42
40	GND	14	28	VCC	41
40	GND	14	28	VCC	41



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